



Shri Vaishnav Vidhyapeeth Vishwavidhyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

Diploma in Electronics and Instrumentation Engineering

VI - SEMESTER(2019-22)

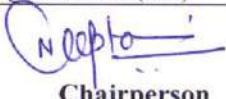
S. No.	Subject Code	Name of Subject	Teaching Scheme/week			CREDITS	Examination Scheme					Total Marks
			L	T	P		Theory			Practical		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	DTEI601	VLSI and VHDL	2	1	2	4	60	20	20	30	20	150
2	BBAI501	Human Values and Professional Ethics	4	0	0	4	60	20	20	0	0	100
3	DTEI603	Bio Medical Instrumentation	2	1	2	4	60	20	20	30	20	150
4	DTEI606	Signal Conditioning and Transducers	2	0	2	3	60	20	20	30	20	150
5	DTEI611/612/613	Elective	3	1	2	5	60	20	20	30	20	150
6	DTEI605	Major Project	0	0	4	2	0	0	0	30	20	50
TOTAL			13	3	12	22	300	100	100	150	100	750

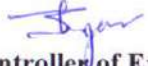
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;


*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Elective	1. Fiber Optics(611)	3	1	2	5	60	20	20	30	20	150
	2. Digital Image Processing(612)	3	1	2	5	60	20	20	30	20	150
	3. Linear integrated circuit(613)	3	1	2	5	60	20	20	30	20	150



Chairperson
 Board of Studies
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Chairperson
 Faculty of Studies
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Controller of Examinations
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya Indore


Joint Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya Indore


Vice Chancellor
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya Indore


 13.1.2022